PATENT
Docket No. 29287/126

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTORS: Kiyokazu NISHIOKA, et al.

SERIAL NO. :   10/053,683

FILED   :   January 24, 2002

FOR   :   PROCESSOR

GROUP   :   2154

EXAMINER :   To Be Assigned

ASSISTANT COMMISSIONER FOR
 PATENTS AND TRADEMARKS
Washington, D.C. 20231

SUPPLEMENTAL PRELIMINARY AMENDMENT

SIR:

Prior to initial examination, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claims 1-4 as follows:

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1. (Amended) A processor, comprising:

a memory for storing an instruction code and data;

an instruction code holding means for holding a plurality of instruction codes read from said

memory; and

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